

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No: Q77191

Group Art Unit: 2826

Examiner: Alexander O. WILLIAMS

For: SEMICONDUCTOR DEVICE HAVING SILICON-DIFFUSED METAL WIRING
LAYER AND ITS MANUFACTURING METHOD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

An Amendment Under 37 C.F.R. § 1.111 is attached hereto for concurrent filing in the above-identified application. The resulting excess claim fee has been calculated as shown below:

	After Amendment		Highest No. Previously Paid For					
All Claims	<u>213</u>	-	<u>212</u>	=	<u>1</u>	X	<u>\$50.00</u>	= <u>\$ 50.00</u>
Independent	<u>18</u>	-	<u>17</u>	=	<u>1</u>	X	<u>\$200.00</u>	= <u>\$200.00</u>
					TOTAL			= \$250.00

EXCESS CLAIM FEE PAYMENT LETTER

U.S. Application No. 10/650,193

Q77191

A check for the statutory fee of \$250.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this letter is enclosed.

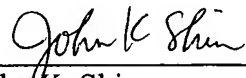
Respectfully submitted,

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WASHINGTON OFFICE

23373

CUSTOMER NUMBER



John K. Shin
Registration No. 48,409

Date: March 2, 2005



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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q77191

Koichi OHTO, et al.

Appln. No.: 10/650,193

Group Art Unit: 2826

Confirmation No.: 6245

Examiner: Alexander O. WILLIAMS

Filed: August 28, 2003

For: SEMICONDUCTOR DEVICE HAVING SILICON-DIFFUSED METAL WIRING
LAYER AND ITS MANUFACTURING METHOD

AMENDMENT UNDER 37 C.F.R. § 1.111

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated November 3, 2004, please amend the above-identified application as follows on the accompanying pages.

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03/04/2005 MBEYENE1 00000083 10650193

02 FC:1201	200.00 OP
03 FC:1202	50.00 OP